

## Murali Tummala

Professor

Mail Code: EC/Tu

Department of Electrical and Computer Engineering Graduate School of Engineering and Applied Sciences

Monterey, CA 93943 Phone: 831 656-2645 Email: mtummala@nps.edu

Web: web.nps.navy.mil/~mtummala

#### **EDUCATION:**

**PhD** - Indian Institute of Technology (Bombay, India), 1984 **MS** - Indian Institute of Technology (Bombay, India), 1981 **BS** - Institution of Engineers (Calcutta, India), 1979

#### **NPS EXPERIENCE:**

- 1998-Present: Associate Chair for Research, Department of Electrical and Computer Engineering
- 1997-Present: Professor, Department of Electrical and Computer Engineering
- 1990-1997: Associate Professor, Department of Electrical and Computer Engineering
- 1986-1990: Assistant Professor, Department of Electrical and Computer Engineering
- 1985-1986: National Research Council Research Associate, Department of Electrical and Computer Engineering

## **OTHER EXPERIENCE:**

 1983 - 1985 - Project Engineer, Advanced Center for Electronics, Indian Institute of Technology, Bombay, India

#### **TEACHING INTERESTS:**

- Signal processing
- Computer networks
- Communications

#### **RESEARCH INTERESTS:**

- Signal processing
- Digital signal compression and transmission
- Wireless networks
- Communications

# **PUBLICATIONS:**

- Barsanti, R. and Tummala, M., "Parameter Estimation for Target Tracking with Uncertain Sensor Positions," IEEE International Conference on Circuits and Systems (ISCAS 2001), Sydney, Australia, 6-9 May 2001.
- Cristi, R. and Tummala, M., "Multirate, multiresolution, recursive Kalman filter," *Journal of Signal Processing*, Vol. 80, pp. 1945-1958, 2000.
- Aziz, A., Tummala, M., and Cristi, R., "Fuzzy Logic Data Correlation Approach in Multisensor-Multitarget Tracking Systems," *Journal of Signal Processing*, July 1999.
- Parker, R.E. and Tummala, M., "Modeling of Layered Video for Low-Bit-Rate Tactical Video Conferencing Applications," *IEEE International Conference on Circuits and Systems (ISCAS 1999)*, Orlando, FL, 30 May -02 June 1999.
- Siomacco, E.M. and Tummala, M., "Parametric modeling of integrated circuit interconnections," *IEEE Trans. on Circuits and Systems II: Analog and Digital Signal Processing*, Vol. 39, No. 6, pp. 377-382, June 1992.
- Tummala, M., "New algorithm for solving block matrix equations with applications in 2-D AR spectral estimation," *IEEE Trans. Signal Processing*, Vol. 39, pp. 759-764, March 1991.

## **KEYWORDS/TECHNOLOGIES:**

•	<b>Keywords:</b> Signal Processing, adaptive filtering, iterative methods, parameter estimation, data compression, wireless networks